For m PTO-1449 (Modified)	U.S. Department of Commerce	Atty. Docket No.	Serial No.
	Patent and Trademark Office	29926/39504	·To be Assigned_
		Applicant	10/669 996
INFORMATION DISC	CLOSURE STATEMENT	Ju-Il Lee	·
		Filing Date	Group 2813
		09/2 4 /03	To be Assigned

U.S. PATENT DOCUMENTS						
*Examiner Initials	Document Number	Issue Date	Name	Class	Subclass	Filing Date if Appropriate
						<u> </u>

FOREIGN PATENT DOCUMENTS						
*Examiner Initials		Document Number Publication Date Country Class Subclass Translation		Translation		
						Yes No

	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)			
D/F	Furumiya et al., High-Sensitivity and No-Crosstalk Pixel Technology for Embedded CMOS Image Sensor, IEEE Transactions on Electron Devices, Vol. 48, No. 10, October 2001, Pages 2221-2227.			
DAK	Nallapati et al., Influence of Plasma Induced Damage During Active Etch on Silicon Defect Generation, 2000 5 th International Symposium on Plasma Process-Induced Damage, May 23-24, Santa Clara, CA, USA, 2000 American Vacuum Society, Pages 61-64.			

EXAMINER DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.